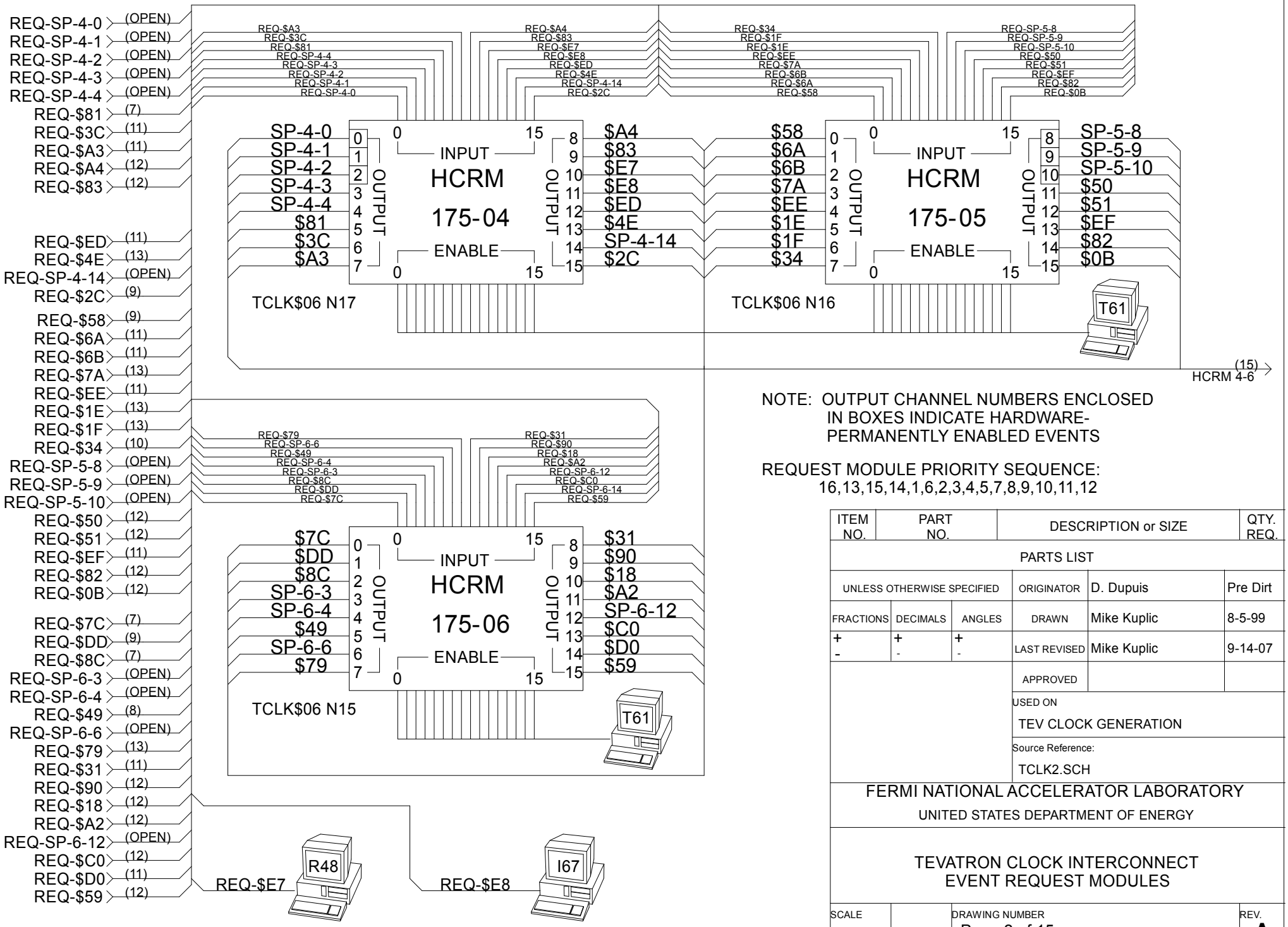

NOTE: OUTPUT CHANNEL NUMBERS ENCLOSED IN BOXES INDICATE HARDWARE-PERMANENTLY ENABLED EVENTS

REQUEST MODULE PRIORITY SEQUENCE:
16,13,15,14,1,6,2,3,4,5,7,8,9,10,11,12

ITEM NO.	PART NO.	DESCRIPTION or SIZE	QTY. REQ.
PARTS LIST			

UNLESS OTHERWISE SPECIFIED			ORIGINATOR	D. Dupuis	Pre Dirt
FRACTIONS	DECIMALS	ANGLES	DRAWN	Mike Kuplic	8-5-99
+ -	+ -	+ -	LAST REVISED	Mike Kuplic	9-14-07
			APPROVED		

USED ON
TEV CLOCK GENERATION

Source Reference:
TCLK2.SCH

FERMI NATIONAL ACCELERATOR LABORATORY
UNITED STATES DEPARTMENT OF ENERGY

TEVATRON CLOCK INTERCONNECT
EVENT REQUEST MODULES

SCALE	DRAWING NUMBER	REV.
1:1	Page 2 of 15	A